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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM FILING DATE CLASS SUBCLASS GAU EXAMINER  
10085716 02/27/2002 324 14 2856 Stevenson

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438 2812

\*\*CONTINUING DATA VERIFIED:  
This appin claims benefit of 60/272,283 02/27/2001

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☒ RESCIND ☐  
Foreign priority claimed ☐ yes ☐ no  
35 USC 119 conditions met ☐ yes ☐ no  
Verified and Acknowledged Examiners's initials  
ATTORNEY DOCKET NO  
CYPR-PM01010  
TITLE : Method of performing back-end manufacturing of an integrated circuit device  
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

| NOTICE OF ALLOWANCE MAILED  |           | CLAIMS ALLOWED     |  |                      |
|---|-----------|--------------------|--|----------------------|
|   |           | Assistant Examiner |  | Total Claims         |
|   |           |                    |  | Print Claim for Q.G. |
| ISSUE FEE   |           | DRAWING            |  |                      |
| Amount Due  | Date Paid | Sheets Drwg.       |  | Print Fig.           |
|   |           | Primary Examiner   |  |                      |
| <input type="checkbox"/> TERMINAL<br>DISCLAIMER   |           | PREPARED FOR ISSUE |  | Application Examiner |
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